

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/050,348  
Filing Date ..... January 15, 2002  
Inventor ..... Gurtej S. Sandhu et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Unknown  
Attorney's Docket No. .... MI22-1898  
Title: Transistor Structures

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed in the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

**EV0 77333971**

Respectfully submitted,

Dated: July 25, 2002

By: Jennifer J. Taylor  
Jennifer J. Taylor, Ph.D.  
Reg. No. 48,711



7-29-02

2813

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Total Number of Pages in This Submission

Application Number	10/050,348
Filing Date	January 15, 2002
First Named Inventor	Gurtej S. Sandhu
Group Art Unit	
Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number MI22-1898

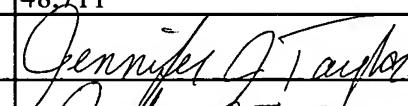
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### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Jennifer J. Taylor, Ph.D.; Wells St. John P.S.; Reg. No. 48,711
Signature	
Date	July 25, 2002

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